

FIG. 1

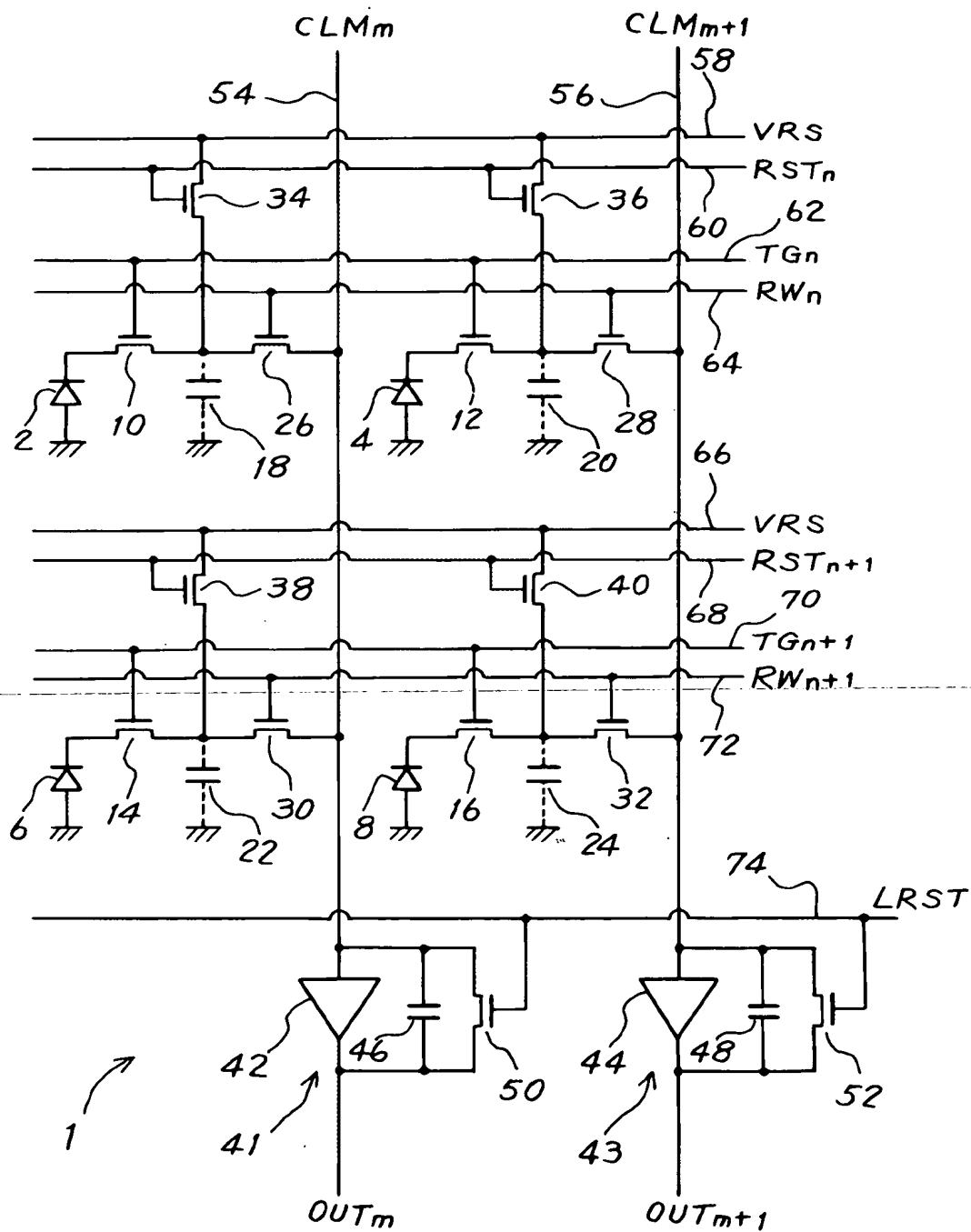


FIG. 2

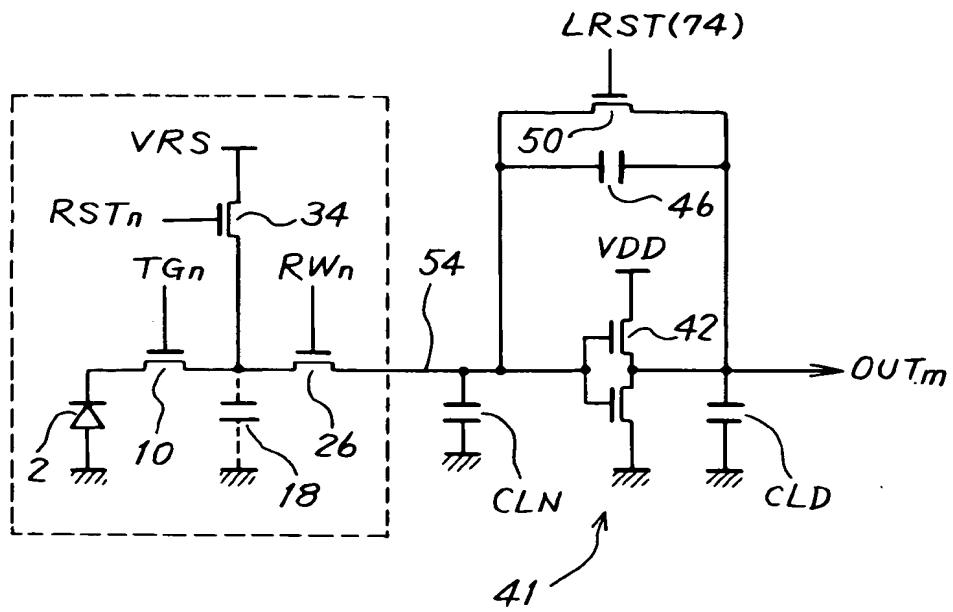


FIG. 3

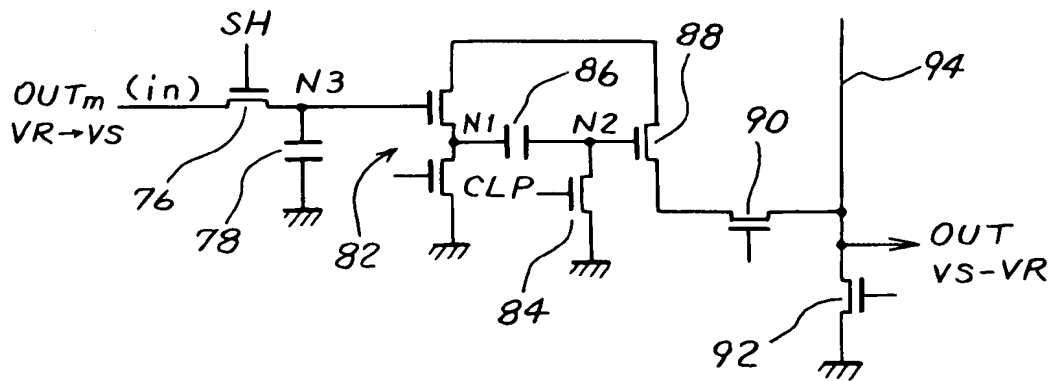


FIG. 4A

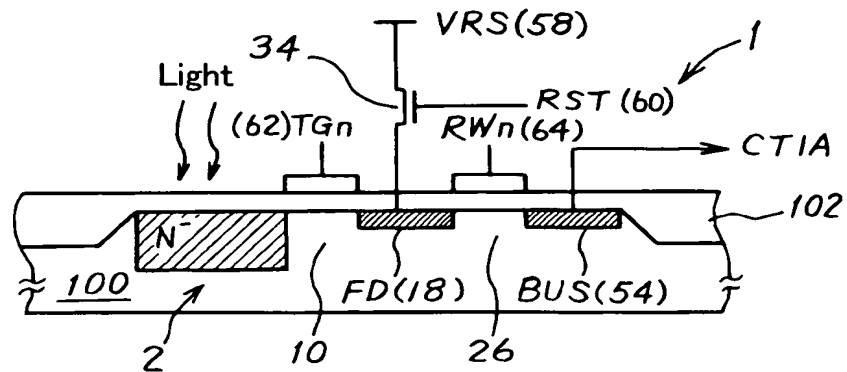


FIG. 4B

Reset Period t1

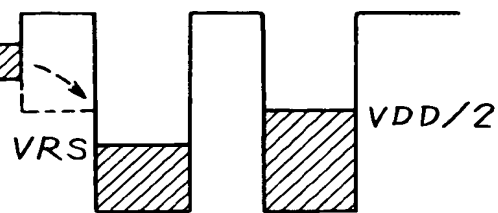


FIG. 4C

Reset Level

Read out Period t2

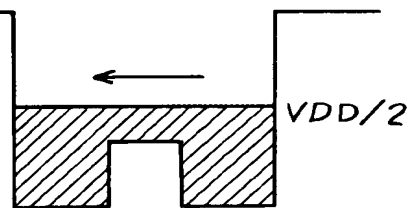


FIG. 4D

Light Integral Value

Read out Period t3

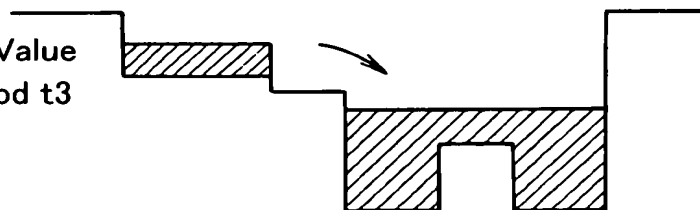


FIG. 4E

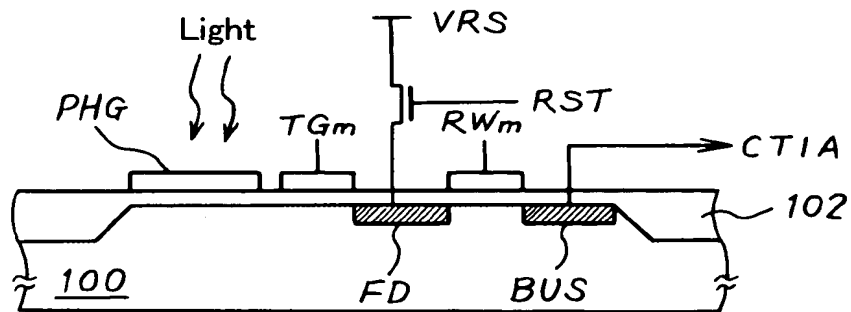


FIG. 6

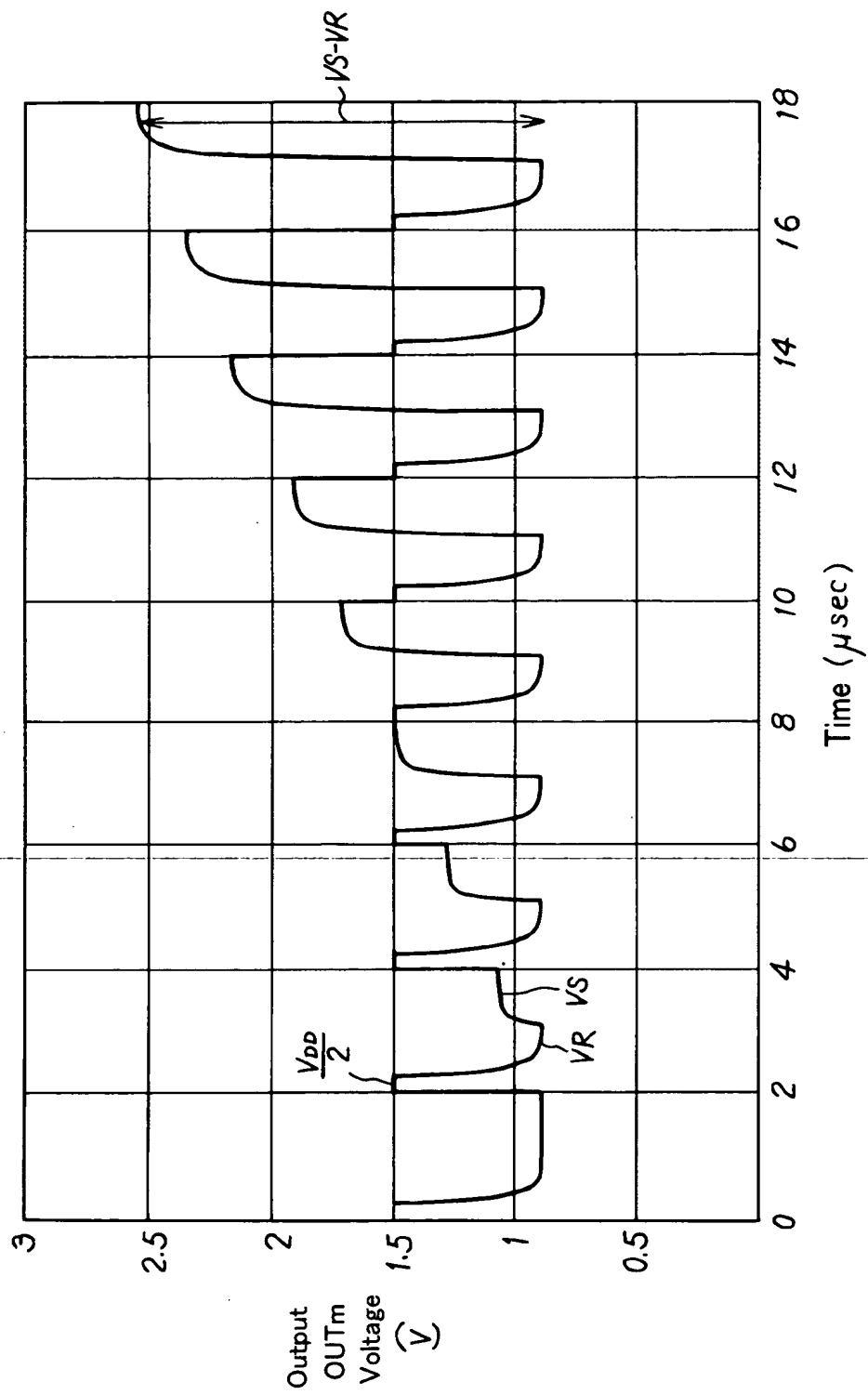


FIG. 7

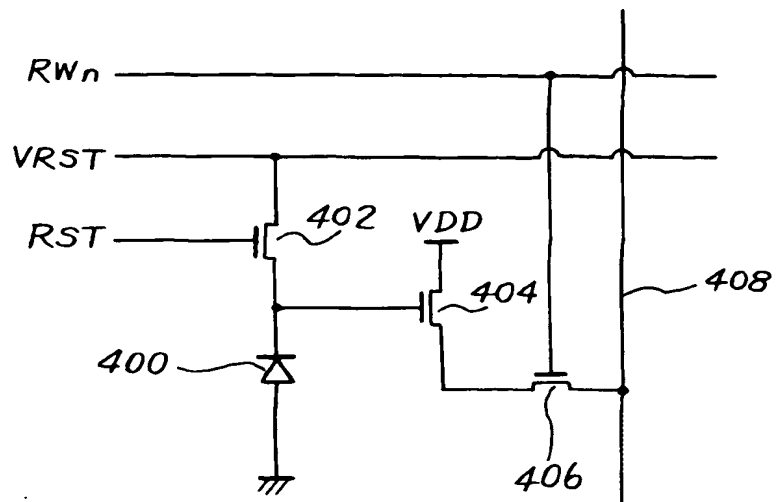


FIG. 8

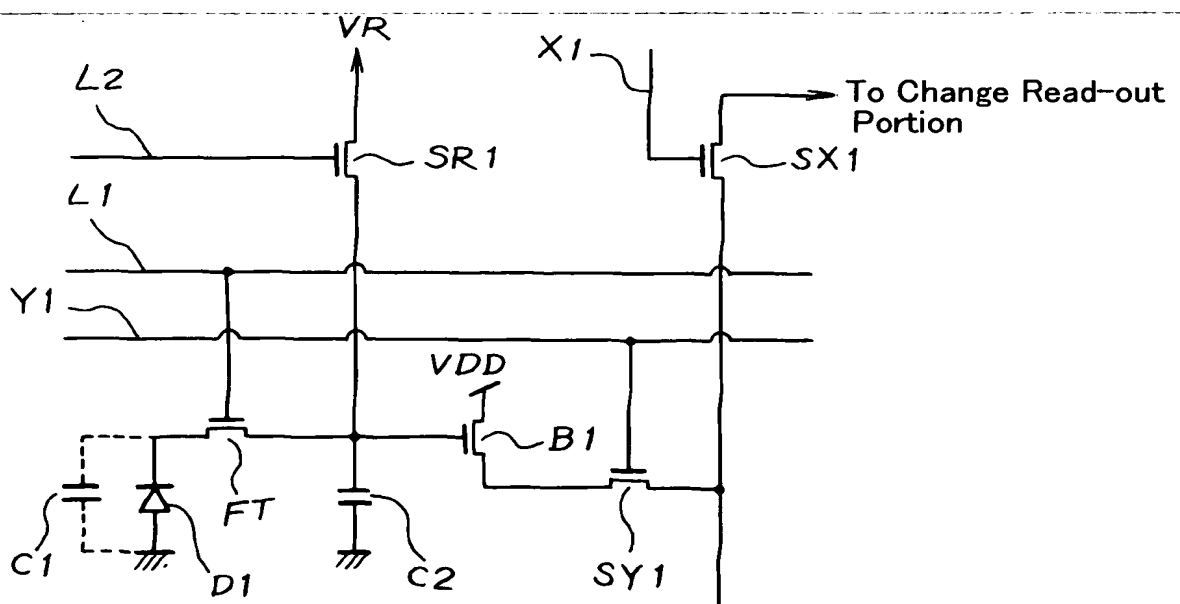


FIG. 9

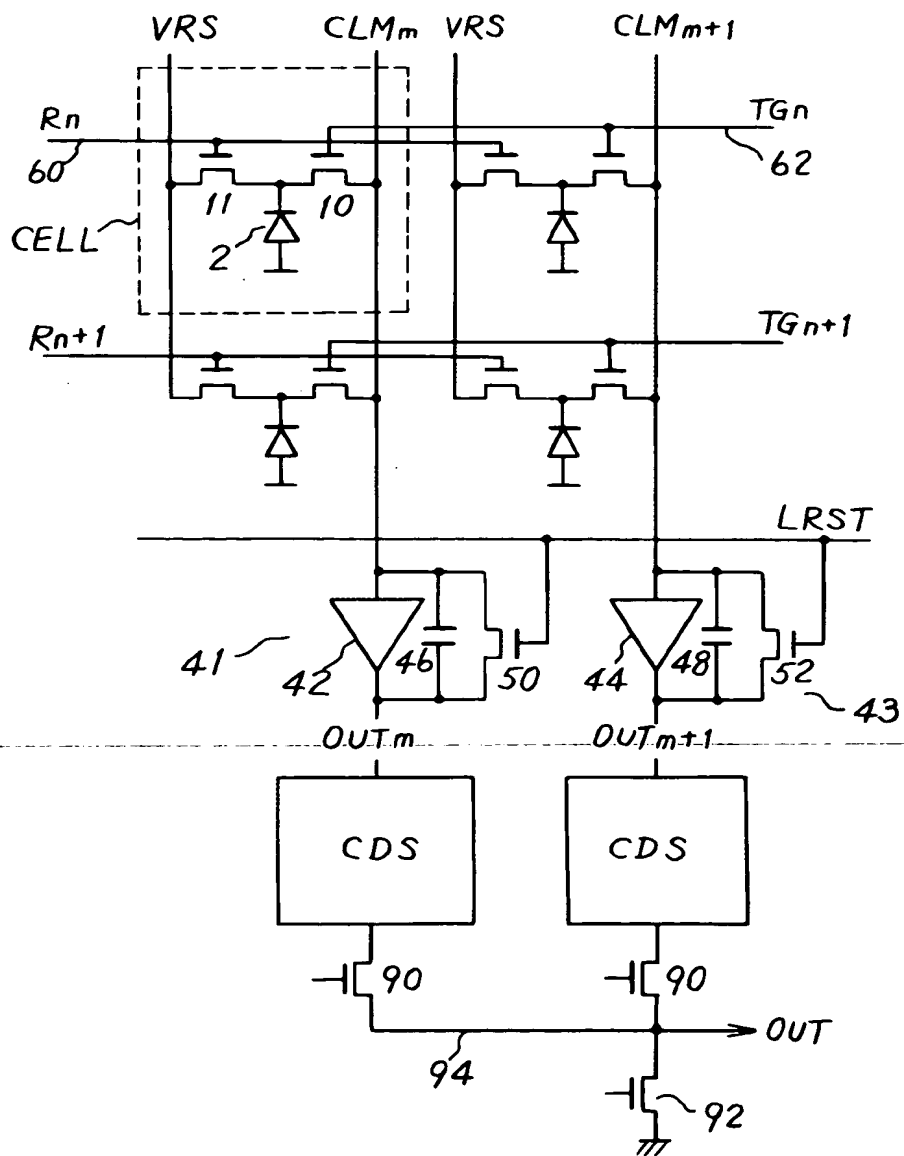


FIG. 10A

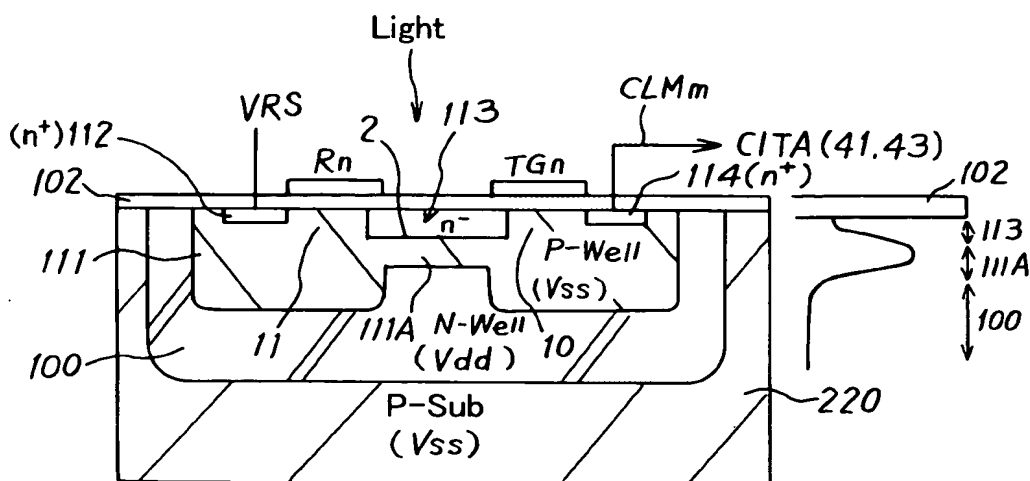


FIG. 10B

t_{10}, t_{11}

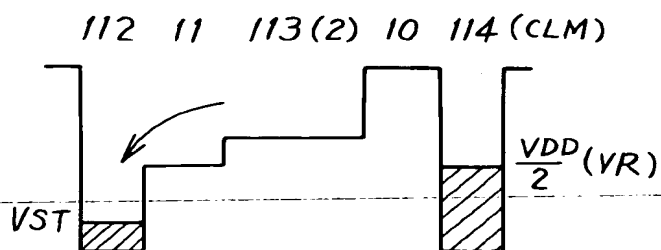


FIG. 10C

t_{12}

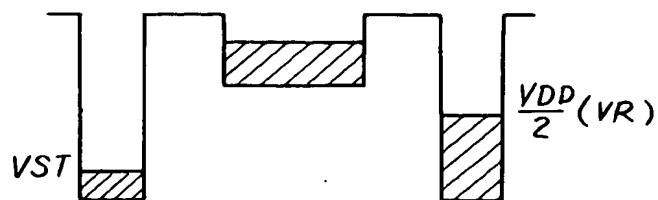


FIG. 10D

t_{13}

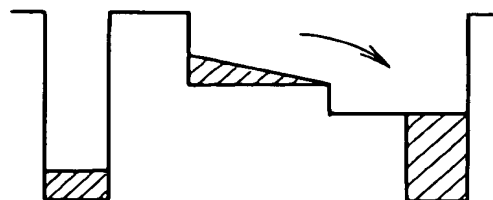
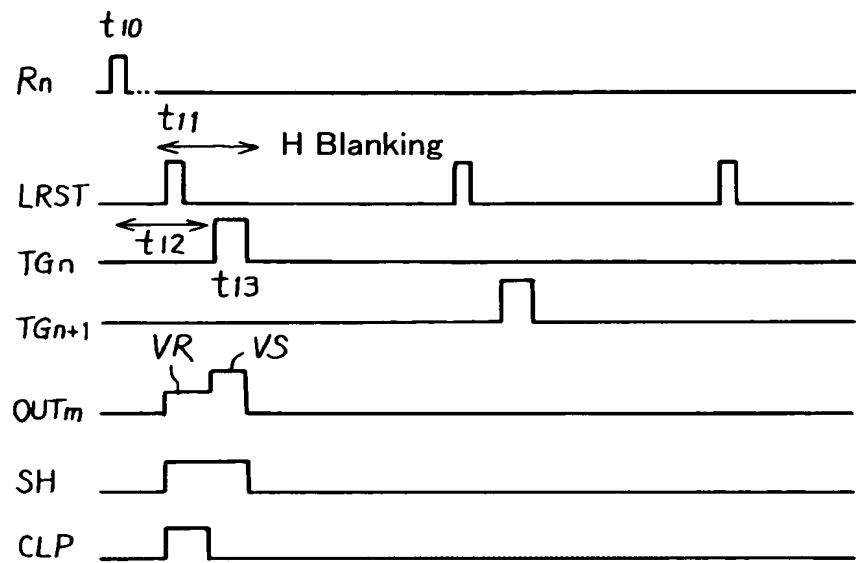
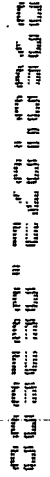
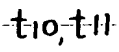


FIG. 11



[illegible] t_{10}, t_{11} 

t12



t13



FIG. 13

